EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	13	257/666,669,670,712.ccls. and (lead adj frame) and chip and (mounting adj board) and (opening or open) and (tie adj bar)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 06:54
L3	0	(257/666,669,670,712.ccls. and (lead adj frame) and chip and (mounting adj board) and (opening or open) and (tie adj bar)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/10 06:54
L4	0	("257"/$.ccls. and (lead adj frame) and chip and (mounting adj board) and (opening or open) and (tie adj bar)).clm.	US-PGPUB	OR	ON	2006/07/10 06:54
L5	0	("438"/$.ccls. and (lead adj frame) and chip and (mounting adj board) and (opening or open) and (tie adj bar)).clm.	US-PGPUB	OR	ON	2006/07/10 06:55
L6	0	(semiconductor and (lead adj frame) and chip and (mounting adj board) and (opening or open) and (tie adj bar)).clm.	US-PGPUB	OR	ON	2006/07/10 06:55
L7	3	(semiconductor and (lead adj frame) and chip and (opening or open) and (tie adj bar)).clm.	US-PGPUB	OR	ON	2006/07/10 06:55
S1	3987	(257/666).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/07/10 06:52
S2	509	(257/670).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/12 09:29
S7	23	nguyen.inv. and amd	USPAT	OR	ON	2005/12/13 14:13
S8	4497	nguyen.inv.	USPAT	OR	ON	2005/12/13 14:13
S9	1076	S8 and interconnect$4	USPAT	OR	ON	2005/12/13 14:13
S10	13	S9 and anneal$4 and cap	USPAT	OR	ON	2005/12/13 14:15
S11	43	S8 and anneal$4 and cap	USPAT	OR	ON	2005/12/13 14:15

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